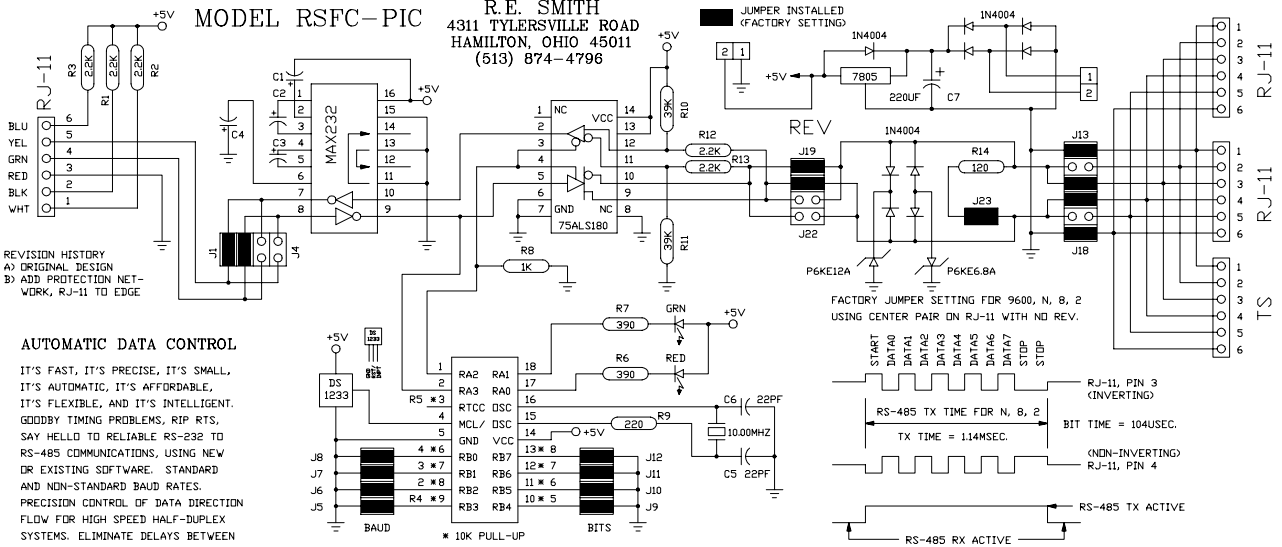


# MODEL RSFC-PIC

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REVISION HISTORY  
A) ORIGINAL DESIGN  
B) ADD PROTECTION NETWORK, RJ-11 TO EDGE

## AUTOMATIC DATA CONTROL

IT'S FAST, IT'S PRECISE, IT'S SMALL, IT'S AUTOMATIC, IT'S AFFORDABLE, IT'S FLEXIBLE, AND IT'S INTELLIGENT. GOODBYE TIMING PROBLEMS, RIP RTS, SAY HELLO TO RELIABLE RS-232 TO RS-485 COMMUNICATIONS, USING NEW OR EXISTING SOFTWARE. STANDARD AND NON-STANDARD BAUD RATES. PRECISION CONTROL OF DATA DIRECTION FLOW FOR HIGH SPEED HALF-DUPLEX SYSTEMS. ELIMINATE DELAYS BETWEEN CHARACTERS, AND SLAVE RESPONSES.

NOTE: REMOVE POWER TO CHANGE BAUD/BIT JUMPERS (J5 - J12)

BAUD	J5	J6	J7	J8
300	REMOVE	REMOVE	REMOVE	REMOVE
600	REMOVE	REMOVE	REMOVE	INSTALL
1200	REMOVE	REMOVE	INSTALL	REMOVE
2400	REMOVE	REMOVE	INSTALL	INSTALL
4800	REMOVE	INSTALL	REMOVE	REMOVE
9600	REMOVE	INSTALL	REMOVE	INSTALL
19200	REMOVE	INSTALL	INSTALL	REMOVE
38400	REMOVE	INSTALL	INSTALL	INSTALL
57600	INSTALL	REMOVE	REMOVE	REMOVE
76800	INSTALL	REMOVE	REMOVE	INSTALL
115200	INSTALL	REMOVE	INSTALL	REMOVE
RESVD	INSTALL	REMOVE	INSTALL	INSTALL
RESVD	INSTALL	INSTALL	REMOVE	REMOVE
RESVD	INSTALL	INSTALL	REMOVE	INSTALL
RXONLY	INSTALL	INSTALL	INSTALL	REMOVE
9600*	INSTALL	INSTALL	INSTALL	INSTALL

NOTES: AT 115.2K BAUD THE START BIT ERROR IS APPROXIMATELY 20% OF THE BIT TIME OR ABOUT 16USEC. THE MODEL RSFR-1 REDUCES THIS ERROR TO UNDER 200NS (2.5%) FOR CRITICAL APPLICATIONS AND IS DPTO-INSTALLED.

TOTAL BITS	START	PARITY	DATA	STOP	EXAMPLE
6	REMOVE	REMOVE	REMOVE	REMOVE	
6-1/2	REMOVE	REMOVE	REMOVE	INSTALL	
7	REMOVE	REMOVE	INSTALL	REMOVE	
7-1/2	REMOVE	REMOVE	INSTALL	INSTALL	
8	REMOVE	INSTALL	REMOVE	REMOVE	
8-1/2	REMOVE	INSTALL	REMOVE	INSTALL	
9	REMOVE	INSTALL	INSTALL	REMOVE	N, 7, 1
9-1/2	REMOVE	INSTALL	INSTALL	INSTALL	
10	INSTALL	REMOVE	REMOVE	REMOVE	N, 8, 1
10-1/2	INSTALL	REMOVE	REMOVE	INSTALL	
11	INSTALL	REMOVE	INSTALL	REMOVE	N, 8, 2
11-1/2	INSTALL	REMOVE	INSTALL	INSTALL	
12	INSTALL	INSTALL	REMOVE	REMOVE	
12-1/2	INSTALL	INSTALL	REMOVE	INSTALL	
13	INSTALL	INSTALL	INSTALL	REMOVE	
13-1/2	INSTALL	INSTALL	INSTALL	INSTALL	

PC COMMAND STRING TO SLAVE UNIT TX DATA  
RX DATA  
TYPICAL RTS FOR NON-AUTOMATIC UNITS  
MASTER DATA FLOW  
SLAVE UNIT DATA FLOW  
RS-485 INVERT  
RS-485 NON-INV  
TX/RX CONTROL (75ALS180, PINS-3,4)  
CRITICAL DATA DIRECTION WINDOW  
DATA DIRECTION REVERSAL MUST OCCUR IN MICRO-SECONDS FOR RELIABLE HIGH-SPEED RS-485 COMMUNICATIONS. PC RTS CONTROL IS OFTEN TOO SLOW OR NOT IMPLEMENTED.

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